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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO		
09/651,260	08/30/2000	Jong Sang Baek	8733.A285	1251		
30827 75	590 01/04/2005		EXAMINER			
MCKENNA LONG & ALDRIDGE LLP			NGUYEN, KIMNHUNG T			
1900 K STREE WASHINGTO	,		ART UNIT	PAPER NUMBER		
	•		2674			
			DATE MAILED: 01/04/2009	DATE MAILED: 01/04/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Applicati	on No.	Applicant(s)			
Office Action Summary		09/651,2	60	BAEK, JONG SANG			
		Examine	r	Art Unit			
_			g Nguyen	2674			
Period f	The MAILING DATE of this communication Reply	ion appears on th	e cover sheet with the	e correspondence address -			
A SH	ORTENED STATUTORY PERIOD FOR	REPLY IS SET 1	O EXPIRE 3 MONT	H(S) FROM			
THE - External control	MAILING DATE OF THIS COMMUNICAT ensions of time may be available under the provisions of 37 r SIX (6) MONTHS from the mailing date of this communicate period for reply specified above is less than thirty (30) day of period for reply is specified above, the maximum statutory ure to reply within the set or extended period for reply will, but reply received by the Office later than three months after the patent term adjustment. See 37 CFR 1.704(b).	TION. CFR 1.136(a). In no exation. ys, a reply within the sta y period will apply and w by statute, cause the app	vent, however, may a reply be tutory minimum of thirty (30) o rill expire SIX (6) MONTHS fro blication to become ABANDO	timely filed days will be considered timely. om the mailing date of this communication. NED (35 U.S.C. § 133).			
Status							
1)[X]	Responsive to communication(s) filed or	n 28 June 2004.					
2a)□							
3)□							
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Disposit	ion of Claims						
4)⊠	Claim(s) 1-6 is/are pending in the application	ation.					
	4a) Of the above claim(s) is/are w	vithdrawn from co	nsideration.				
5)□	Claim(s) is/are allowed.						
6)⊠	Claim(s) 1-3 and 6 is/are rejected.						
7)🖂	Claim(s) 4 and 5 is/are objected to.						
8)[Claim(s) are subject to restriction	and/or election r	equirement.				
Applicat	ion Papers						
9)□	The specification is objected to by the Ex	kaminer.					
10)	The drawing(s) filed on is/are: a)[accepted or b	☐ objected to by the	e Examiner.			
	Applicant may not request that any objection	to the drawing(s) I	oe held in abeyance. S	See 37 CFR 1.85(a).			
	Replacement drawing sheet(s) including the	•	J.,	, ,).		
11)	The oath or declaration is objected to by	the Examiner. No	ote the attached Office	ce Action or form PTO-152.			
Priority (under 35 U.S.C. § 119						
	Acknowledgment is made of a claim for for All b) Some * c) None of:			a)-(d) or (f).			
	1. Certified copies of the priority docu			stian Na			
	2. Certified copies of the priority docu3. Copies of the certified copies of th		* *				
	application from the International E			ved iii tiiis ivational Stage			
* (See the attached detailed Office action for	•	,	ved.			
Attachmen	it(s)						
Notice of References Cited (PTO-892)			4) Interview Summa				
	e of Draftsperson's Patent Drawing Review (PTO-9 mation Disclosure Statement(s) (PTO-1449 or PTO/		Paper No(s)/Mail 5) Notice of Informal	Date Patent Application (PTO-152)			
Paper No(s)/Mail Date			6) Other:	.,,			

DETAILED ACTION

This Application has been examined. The claims 1-6 are pending. The examination results are as following.

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1-2 and 6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Schiefer et al. (US 6,177,922).

Regarding claim 1, Schiefer et al. disclose in figures 1-4, a flat panel display device (140) with a multi-timing controller (DCLK, DEN, DHSYNC, DVSYNC, fig.3) comprising a display standard (see VGA, XGA timing compatible displays, see column 5, lines 53-55); an interface (130, figure 1) receiving a timing data inputted from the exterior thereof and a control signal corresponding to the display standard; a timing controller (330) for latching and outputting the timing data inputted from the interface, and for generating and outputting timing signals for driving the liquid crystal display panel based on the control signal; and a driving circuit for receiving the timing signals outputted from the timing controller to display a picture corresponding to the display standard, wherein said timing controller includes a decoder (120, fig. 1) and a timing generator (430, fig. 4), wherein timing generation information corresponding to a plurality of display standards is stored by the decoder (see displays of VGA,XGA, see col.

5., lines 53-55 2), wherein the decoder (120) may output to the timing generator, timing information corresponding to the timing data, and wherein timing generator outputs timing signals corresponding to the timing information and the control signal (see column 11, lines 46-60). However, Schiefer et al. does not disclose that the display device is a liquid crystal display. It would have been obvious to one of ordinary skill in the art at the time the invention to have the flat display panel is a liquid crystal display, because the background of the invention does teach that the flat panel display which includes liquid crystal display (see Schichiefer et al., see col. 1., lines 46-47).

Regarding claim 2, Schiefer et al. discloses that the display device further comprising a dip switch (see lock event, see col. 17, lines 3-15) for selecting the timing data corresponding to the display standard.

Regarding claim 6, Schiefer et al. discloses wherein the display standard is selected from SVGA (see col. 6,lines 8-9).

3. Claim 3 is rejected under 35 U.S.C. 103(a) as being unpatentable over Schiefer et al. (US 6,177,922) in view of Barshinger (US 5,049,864).

Schiefer et al. discloses every feature of the claimed invention as discusses above. However, Schiefer et al. does not disclose wherein the decoder consists of a memory for storing certain timing information and a multiplexor for selecting any one of the timing information stored in the memory. Barshinger discloses in figure 1, the LCD (26, 28, 30) having decoder and mutiplexer (22) are stored in the memory (14, see column 4, lines 6-15). It would have been obvious to one of ordinary skill in the art at the time the invention was made to implement the teachings of the LCD having decoder and mutiplexer are stored in the memory as taught by

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Barshinger into the system of Schiefer et al. because this would be made available to the decoder as 8 bit numbers (see column 4, lines 29-33).

Allowable Subject Matter

4. Claims 4-5 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter:

The present invention is directed a liquid crystal display device with a multi-timing controller comprising a liquid crystal display panel having a display standard receiving a timing data inputted from the exterior thereof and a control signal corresponding to the display standard; a timing controller for latching and outputting the timing data inputted from the interface, and for generating and outputting timing signals for driving the liquid crystal display panel based on the control signal; and a driving circuit for receiving the timing signals outputted from the timing controller to display a picture corresponding to the display standard, wherein said timing controller includes a decoder, and a timing generator, wherein timing generation information corresponding to a plurality of display standards is stored by the decoder. The combination of Schiefer et al. (US 6,177,922) and Barshinger (US 5,049,864) show a similar system having multi-timing controller and a plurality of display standards is stored by a decoder. However, they fail to teach wherein the timing generator includes a first controller for generating the timing signal corresponding to the timing information selected from the decoder, a second, a

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third, a fourth, and a fifth controller for always equally keeping the polarity of the horizontal/vertical synchronizing signal as claim 4.

Response to Arguments

5. Applicant's arguments with respect to claims 1-3, and 6 filed on 6-28-04 have been considered but are most in view of the new ground(s) of rejection.

Correspondence

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kimnhung Nguyen whose telephone number (703) 308-0425.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, RICHARD A HJERPE can be reached on (703) 305-4709.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D. C. 20231

Or faxed to:

(703) 872-9314 (for Technology Center 2600 only).

Hand-delivery response should be brought to: Crystal Park II, 2121 Crystal Drive, Arlington, VA Sixth Floor (Receptionist).

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Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377.

Kimnhung Nguyen December 29, 2004

> ALEXANDER EISEN PRIMARY EXAMINER